

Sommario

@cnna@	Terminal	Symbol	Description	Address	
	1A		Output + 24V dc ¹⁾		
	2A	PHA1	Phase A	@slot@.INP17	@slot@.CNT01
	3A	PHB1	Phase B	@slot@.INP18	
	4A	Z1	Z	1.INT01	
	5A	0V	Common for count inputs		
	6A	0V			
	7A	0V			
	1B		Output + 24V dc ³⁾		
	2B	PHA1+	+ PHA	@slot@.INP17	@slot@.CNT01
	3B	PHB1+	+ PHB	@slot@.INP18	
	4B	Z1+	+ Z	1.INT01	
	5B	PHA1-	- PHA		
	6B	PHB1-	- PHB		
	7B	Z1-	- Z		

^{1), 3)} Used to power the encoder. See [Connection examples](#).

²⁾ **PNP/Push-Pull type count input configuration:**

Terminal 5B: connect to terminal 5A

Terminal 6B: connect to terminal 6A

Terminal 7B: connect to terminal 7A

@cnnb@	Terminal	Symbol	Description	Address	
	1A		Output + 24V dc ¹⁾		
	2A	PHA2	Phase A	@slot@.INP19	@slot@.CNT02
	3A	PHB2	Phase B	@slot@.INP20	
	4A	Z2	Z	1.INT02	
	5A	0V	Common for count inputs		
	6A	0V			
	7A	0V			
	1B		Output + 24V dc ³⁾		
	2B	PHA2+	+ PHA	@slot@.INP19	@slot@.CNT02
	3B	PHB2+	+ PHB	@slot@.INP20	
	4B	Z2+	+ Z	1.INT02	
	5B	PHA2-	- PHA		
	6B	PHB2-	- PHB		
	7B	Z2-	- Z		

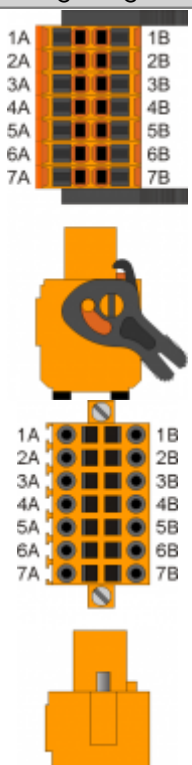
^{1), 3)} Used to power the encoder. See [Connection examples](#).

²⁾ **PNP/Push-Pull type count input configuration:**

Terminal 5B: connect to terminal 5A

Terminal 6B: connect to terminal 6A

Terminal 7B: connect to terminal 7A

@cnnc@	Terminal	Symbol	Description	Address	
	1A		Output + 24V dc ¹⁾		
	2A	PHA3	Phase A	@slot@.INP21	@slot@.CNT03
	3A	PHB3	Phase B	@slot@.INP22	
	4A	Z3	Z	1.INT03	FREQ1 ³⁾
	5A	0V	Common for count inputs		
	6A	0V			
	7A	0V			
	1B		Output + 24V dc ⁴⁾		
	2B	PHA3+	+ PHA	@slot@.INP21	@slot@.CNT03
	3B	PHB3+	+ PHB	@slot@.INP22	
	4B	Z3+	+ Z	1.INT03	FREQ1 ⁵⁾
	5B	PHA3-	- PHA		
	6B	PHB3-	- PHB		
	7B	Z3-	- Z		

^{1), 4)} Used to power the encoder. See [Connection examples](#).

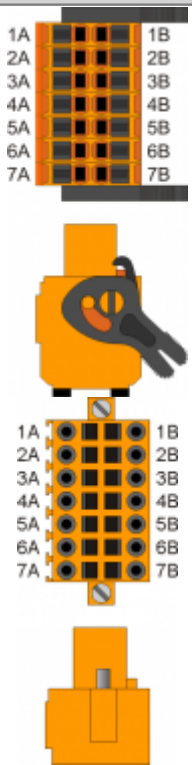
²⁾ **PNP/Push-Pull type count input configuration:**

Terminal 5B: connect to terminal 5A

Terminal 6B: connect to terminal 6A

Terminal 7B: connect to terminal 7A

^{3), 5)} Can be used as frequency input for a FREQ device, indicating 1 in the device declaration

@cnnd@	Terminal	Symbol	Description	Address	
	1A		Output + 24V dc ¹⁾		
	2A	PHA4	Phase A	@slot@.INP23	@slot@.CNT04
	3A	PHB4	Phase B	@slot@.INP24	
	4A	Z4	Z	1.INT04	FREQ2 ³⁾
	5A	0V	Common for count inputs		
	6A	0V			
	7A	0V			
	1B		Output + 24V dc ⁴⁾		
	2B	PHA4+	+ PHA	@slot@.INP23	@slot@.CNT04
	3B	PHB4+	+ PHB	@slot@.INP24	
	4B	Z4+	+ Z	1.INT04	FREQ2 ⁵⁾
	5B	PHA4-	- PHA		
	6B	PHB4-	- PHB		
	7B	Z4-	- Z		

^{1), 4)} Used to power the encoder. See [Connection examples](#).

²⁾ **PNP/Push-Pull type count input configuration:**

Terminal 5B: connect to terminal 5A

Terminal 6B: connect to terminal 6A

Terminal 7B: connect to terminal 7A

^{3), 5)} Can be used as frequency input for a FREQ device, indicating 2 in the device declaration

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