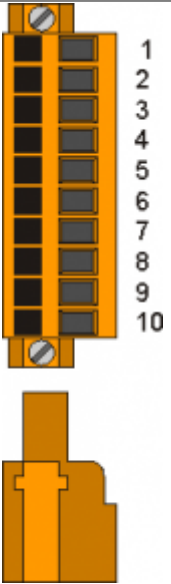
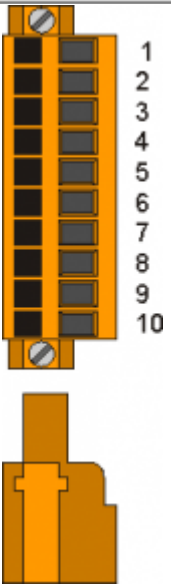


Sommario

@cnna@	Terminal	Simbol	Description	Address
	1	V+	Output power input (12÷28V dc)	
	2	O1	Digital output 1	@slot@.OUT01
	3	O2	Digital output 2	@slot@.OUT02
	4	O3	Digital output 3	@slot@.OUT03
	5	O4	Digital output 4	@slot@.OUT04
	6	O5	Digital output 5	@slot@.OUT05
	7	O6	Digital output 6	@slot@.OUT06
	8	O7	Digital output 7	@slot@.OUT07
	9	O8	Digital output 8	@slot@.OUT08
	10	V-	Output power input (0V dc)	

@cnnb@	Terminal	Simbol	Description	Address
	1	V+	Output power input (12÷28V dc)	
	2	O9	Digital output 9	@slot@.OUT09
	3	O10	Digital output 10	@slot@.OUT10
	4	O11	Digital output 11	@slot@.OUT11
	5	O12	Digital output 12	@slot@.OUT12
	6	O13	Digital output 13	@slot@.OUT13
	7	O14	Digital output 14	@slot@.OUT14
	8	O15	Digital output 15	@slot@.OUT15
	9	O16	Digital output 16	@slot@.OUT16
	10	V-	Output power input (0V dc)	

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